IN THE CLAIMS

Please cancel claim 31 and amend the remaining claims as shown.

1.   (Previously presented)   A method of dynamically adapting wireless link parameters, comprising:

determining a measure of errors occurring in communication over a wireless link;

in a case that the measure of errors corresponds to more errors than a first predetermined threshold, changing from a first set of wireless link parameters to a second set of wireless link parameters, the second set of wireless link parameters cörresponding higher error tolerance than the first set of wireless link parameters; and

in a case that the measure of errors corresponds to fewer errors than a second predetermined threshold, changing from the first set of wireless link parameters to a third set of wireless link parameters, the third set of wireless link parameters corresponding to lower error tolerance than the first set of wireless link parameters;

wherein:

each set of wireless link parameters corresponds to a relationship between throughput efficiency and error rate;

the first predetermined threshold corresponds to intersection of the relationship for the first set of wireless link parameters with the relationship for the second set of wireless link parameters; and

the second predetermined threshold corresponds to intersection of the relationship for the first set of wireless link parameters with the relationship for the third set of wireless link parameters.

2.   (Original)   A method as in claim 1, wherein the measure of errors is determined by monitoring a number of NACK messages and a number of ACK messages that occur.

3.   (Original)   A method as in claim 2, wherein it is determined that the measure of errors corresponds to more errors than the first predetermined threshold when more than a predetermined number of NACK messages occur in succession.

4.   (Original)   A method as in claim 2, wherein it is determined that the measure of errors corresponds to fewer errors than the second predetermined threshold when more than a predetermined number of ACK messages occur in succession.

5.   (Original)   A method as in claim 1, wherein each set of wireless link parameters includes a modulation scheme.

6.   (Original)   A method as in claim 1, wherein each set of wireless link parameters includes a symbol rate.

7.   (Original)   A method as in claim 1, wherein each set of wireless link parameters includes an error correction scheme.

8.   (Original)   A method as in claim 1, wherein each set of wireless link parameters includes a modulation scheme, a symbol rate, and an error correction scheme.

9.   (Original)   A method as in claim 8, wherein the modulation scheme is Quadrature Amplitude Modulation or Quadrature Phase Shift Keying, the symbol rate is high symbol rate or low symbol rate, and the error correction scheme is high forward error correction or low forward error correction.

10.   (Canceled).

11.   (Previously presented)   An apparatus that dynamically adapts wireless link parameters, comprising:

a wireless link interface to a wireless link;

a processor; and

a memory storing instructions executable by the process to control communication over the wireless link interface, the instructions including the steps of: (a) determining a measure of errors occurring in communication over the wireless link; (b) in a case that the measure of errors corresponds to more errors than a first predetermined threshold, changing from a first set of wireless link parameters to a second set of wireless link parameters, the second set of wireless link parameters corresponding to higher error tolerance than the first set of wireless link parameters; and (c) in a case that the measure of errors corresponds to fewer errors than a second predetermined threshold, changing from the first set of wireless link parameters to a third set of wireless link parameters, the third set of wireless link parameters corresponding to lower error tolerance than the first set of wireless link parameters;

wherein:

each set of wireless link parameters corresponds to a relationship between throughput efficiency and error rate;

the first predetermined threshold corresponds to intersection of the relationship for the first set of wireless link parameters with the relationship for the second set of wireless link parameters; and

the second predetermined threshold corresponds to intersection of the relationship for the first set of wireless link parameters with the relationship for the third set of wireless link parameters.

12. (Original) An apparatus as in claim 11, wherein the measure of errors is determined by monitoring a number of NACK messages and a number of ACK messages that occur.

13. (Original) An apparatus as in claim 12, wherein it is determined that the measure of errors corresponds to more errors than the first predetermined threshold when more than a predetermined number of NACK messages occur in succession.

14. (Original) An apparatus as in claim 12, wherein it is determined that the measure of errors corresponds to fewer errors than the second predetermined threshold when more than a predetermined number of ACK messages occur in succession.

15. (Original) An apparatus as in claim 11, wherein each set of wireless link parameters includes a modulation scheme.

16. (Original) An apparatus as in claim 11, wherein each set of wireless link parameters includes a symbol rate.

17. (Original) An apparatus as in claim 11, wherein each set of wireless link parameters includes an error correction scheme.

18. (Original) An apparatus as in claim 11, wherein each set of wireless link parameters includes a modulation scheme, a symbol rate, and an error correction scheme.

19. (Original) An apparatus as in claim 18, wherein the modulation scheme is Quadrature Amplitude Modulation or Quadrature Phase Shift Keying, the symbol rate is high symbol rate or low symbol rate, and the error correction scheme is high forward error correction or low forward error correction.

20. (Canceled).

21. (Previously presented) A memory including instructions, the instructions executable by a processor to dynamically adapt wireless link parameters, the instructions comprising the steps of:

determining a measure of errors occurring in communication over a wireless link;

in a case that the measure of errors corresponds to more errors than a first predetermined threshold, changing from a first set of wireless link parameters to a second set of wireless link parameters, the second set of wireless link parameters corresponding to higher error tolerance than the first set of wireless link parameters; and

in a case that the measure of errors corresponds to fewer errors than a second predetermined threshold, changing from the first set of wireless link parameters to a third set of wireless link parameters, the third set of wireless link parameters corresponding to lower error tolerance than the first set of wireless link parameters;

wherein:

each set of wireless link parameters corresponds to a relationship between throughput efficiency and error rate;

the first predetermined threshold corresponds to intersection of the relationship for the first set of wireless link parameters with the relationship for the second set of wireless link parameters; and

the second predetermined threshold corresponds to intersection of the relationship for the first set of wireless link parameters with the relationship for the third set of wireless link parameters.

22. (Currently amended) A memory as in claim 21 1, wherein the measure of errors is determined by monitoring a number of NACK messages and a number of ACK messages that occur.

23. (Currently amended) A memory as in claim 22 2, wherein it is determined that the measure of errors corresponds to more errors than the first predetermined threshold when more than a predetermined number of NACK messages occur in succession.

24.   (Currently amended)   A memory as in claim 22 2, wherein it is determined that the measure of errors corresponds to fewer errors than the second predetermined threshold when more than a predetermined number of ACK messages occur in succession.

25.   (Currently amended)   A memory as in claim 21 1, wherein each set of wireless link parameters includes a modulation scheme.

26.   (Currently amended)   A memory as in claim 21 1, wherein each set of wireless link parameters includes a symbol rate.

27.   (Currently amended)   A memory as in claim 21 1, wherein each set of wireless link parameters includes an error correction scheme.

28.   (Currently amended)   A memory as in claim 21 1, wherein each set of wireless link parameters includes a modulation scheme, a symbol rate, and an error correction scheme.

29.   (Currently amended)   A memory as in claim 28 8, wherein the modulation scheme is Quadrature Amplitude Modulation or Quadrature Phase Shift Keying, the symbol rate is high symbol rate or low symbol rate,

and the error correction scheme is high forward error correction or low forward

error correction.

31.   (Canceled).

/ / /